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$A = 1$  and  $B = 1$ ,  $B=1, \neg B = 1$ , the latch state is  $Q = 1$ ,  $Q=1$ ,  $Q = 1$ , (e) Derive a circuit for the  $A B \overline{A B}$  latch using four two-input NOR gates and two inverters. An  $A B \overline{A B}$  latch operates as follows: If  $A = 0$ ,  $A=0, \neg A = 0$  and  $B = 0$ ,  $B=0, \neg B = 0$ , the latch

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state is  $Q = 0; Q=0;\backslash!$

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2.24 (a)  $[(XY)'] + (X' + Y)'Z = X' + Y +$

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Theorem with  $X = (X' + Y)$  2.24 (c)  $[(A'$

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